

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
TRAN, TU-ANH, et al.

§ Patent No.: 6937047 B2

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§ Issue Date: 08-30-2005

Application No.: 10/634484

§

§ Examiner: Vinh P. Nguyen

Filed: 08-05-2003

§

§ Group Art Unit: 2829

Docket No.: SC12598TK

§

Title: INTEGRATED CIRCUIT WITH TEST PAD STRUCTURE AND METHOD OF
TESTING

Certificate of Submission

I hereby certify that this correspondence is being submitted to the USPTO,
Alexandria, VA.

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2 - 12 - 2008

Date of Submission

Teresa Shephard

Signature

Teresa Shephard

Printed Name of Person Signing Certificate

Commissioner for Patents
Alexandria, VA 22313

SUBMISSION OF CERTIFICATE OF CORRECTION

Dear Commissioner:

Enclosed is a Certificate of Correction listing error(s) in the subject patent.

Please enter these corrections. Since the errors appear to be on the part of the United
States Patent Office, there should be no charge.

Respectfully submitted,

Feb 11, 2008
Date

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CERTIFICATE OF CORRECTION

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PATENT NO.: 6937047 B2
APPLICATION NO: 10/634484
DATE: 08-05-2003
FIRST NAMED INVENTOR: TRAN, TU-ANH

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In Column 6, Line 21, Claim No. 7:

Change "(BIST)circuitry" to -(BIST) circuitry—

In Column 6, Line 28, Claim No. 9:

Change "100 μ mx200" to—100 μ m x 200—

In Column 6, Line 37, Claim No. 12:

Change "one lest pad" to —one test pad—

In Column 7, Line 25, Claim No. 28:

Change "plurality bond pads" to — plurality of bond pads--

MAILING ADDRESS OF SENDER (Please do not use customer number below)

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